

SEARCHED	INDEXED
SERIALIZED	FILED
09/15/93	
02/03/00	
Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

④ P.S. SCANNED	O.I.P.E. A.A. Q.A.	PATENT DATE Ne
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/497533	YES	710	712	215	2781 2183 Harkness

APPLICANTS

Samuel Naffziger

2183

TITLE: Find-instructions-and-allocate-ports (FIAP) circuit and method for an out of order processor

PTO-2040
12/99

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ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No: _____	(Primary Examiner) _____ (Date) _____			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date) _____			ISSUE BATCH NUMBER	

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